

PROGRAMMABLE LOGIC INTEGRATED CIRCUIT

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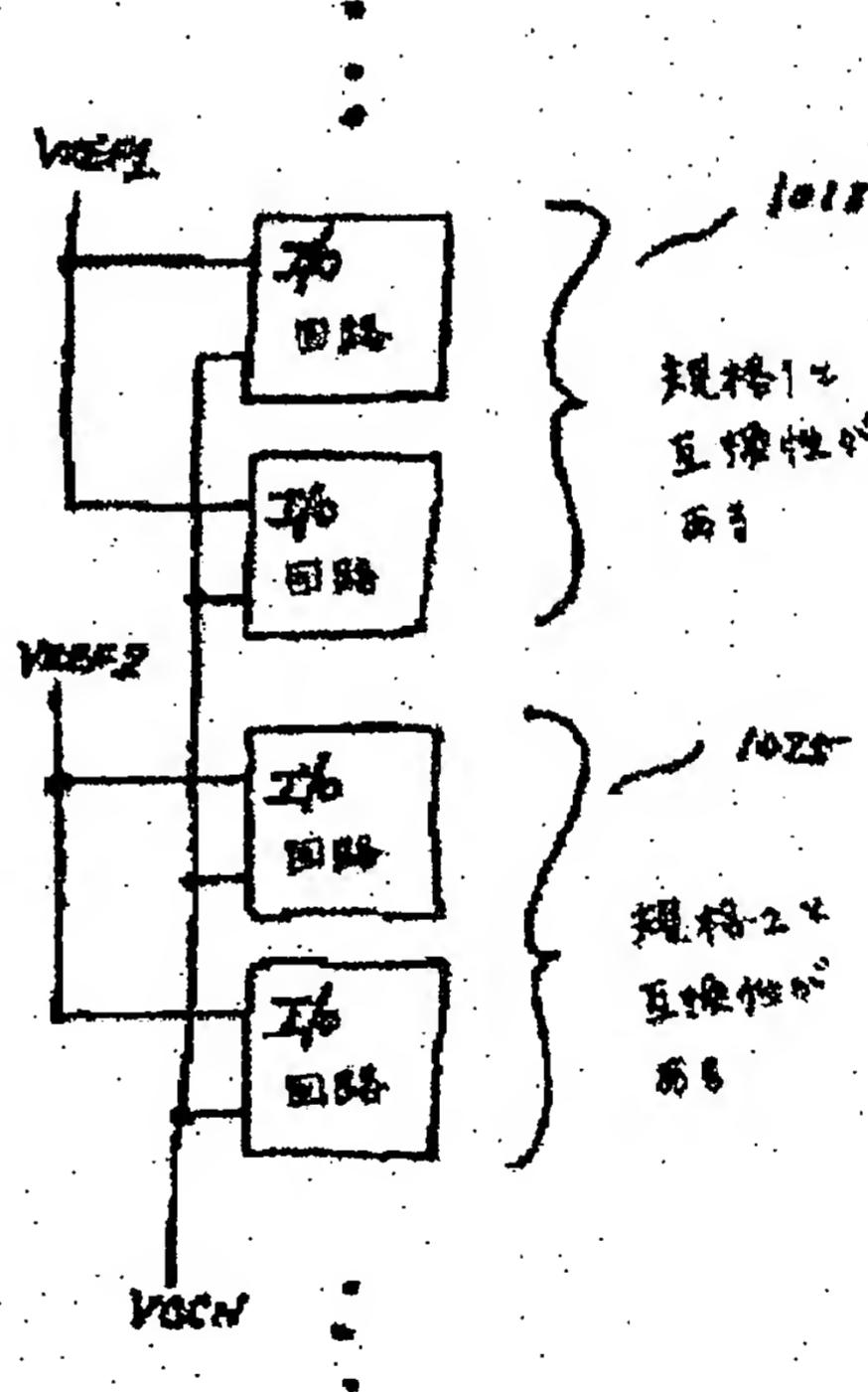
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Abstract of JP2000315731

PROBLEM TO BE SOLVED: To obtain a programmable logic integrated circuit in which each I/O can be reorganized individually by differentiating a first reorganizable reference voltage from a second reorganizable reference voltage and making first and second I/O circuit groups compatible with first and second I/O voltage regulations based on the first and second reorganizable reference voltages. **SOLUTION:** All I/O circuits are connected with same power supply voltage and the power supply voltage is operated with a highest power supply voltage of I/O regulation requested by any one of them. Consequently, two I/O circuits 1015 are connected with a first VREF voltage VREF 1 and two I/O circuits 1025 are connected with a second VREF voltage VREF 2. With such an arrangement, an I/O circuit 1015 is compatible with a first I/O regulation and an I/O circuit 1025 is compatible with a second I/O regulation. First and second I/O regulations are different from each other and one power supply voltage is supplied to all I/O circuits. Consequently, a different power supply voltage is not required.



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